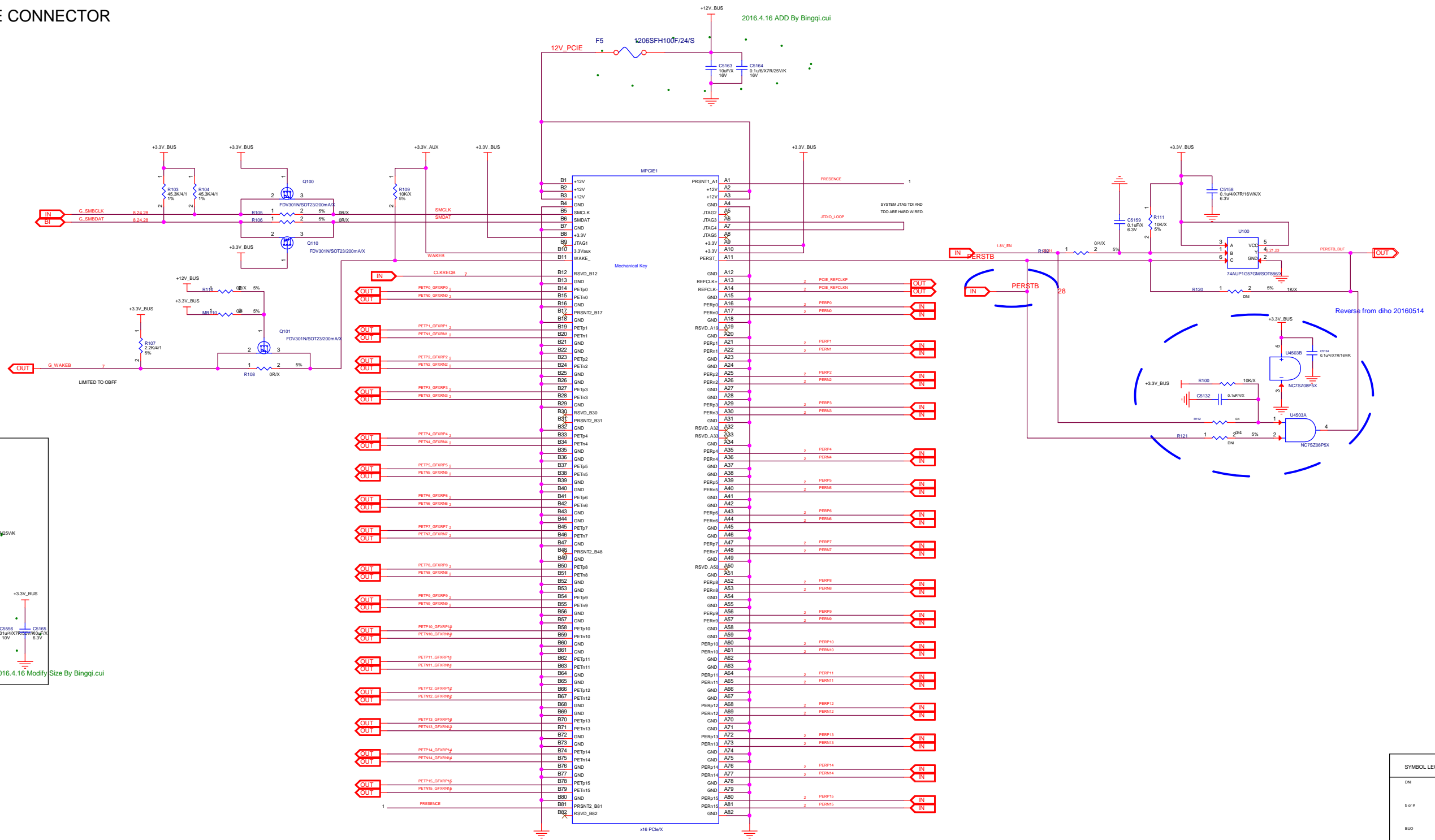


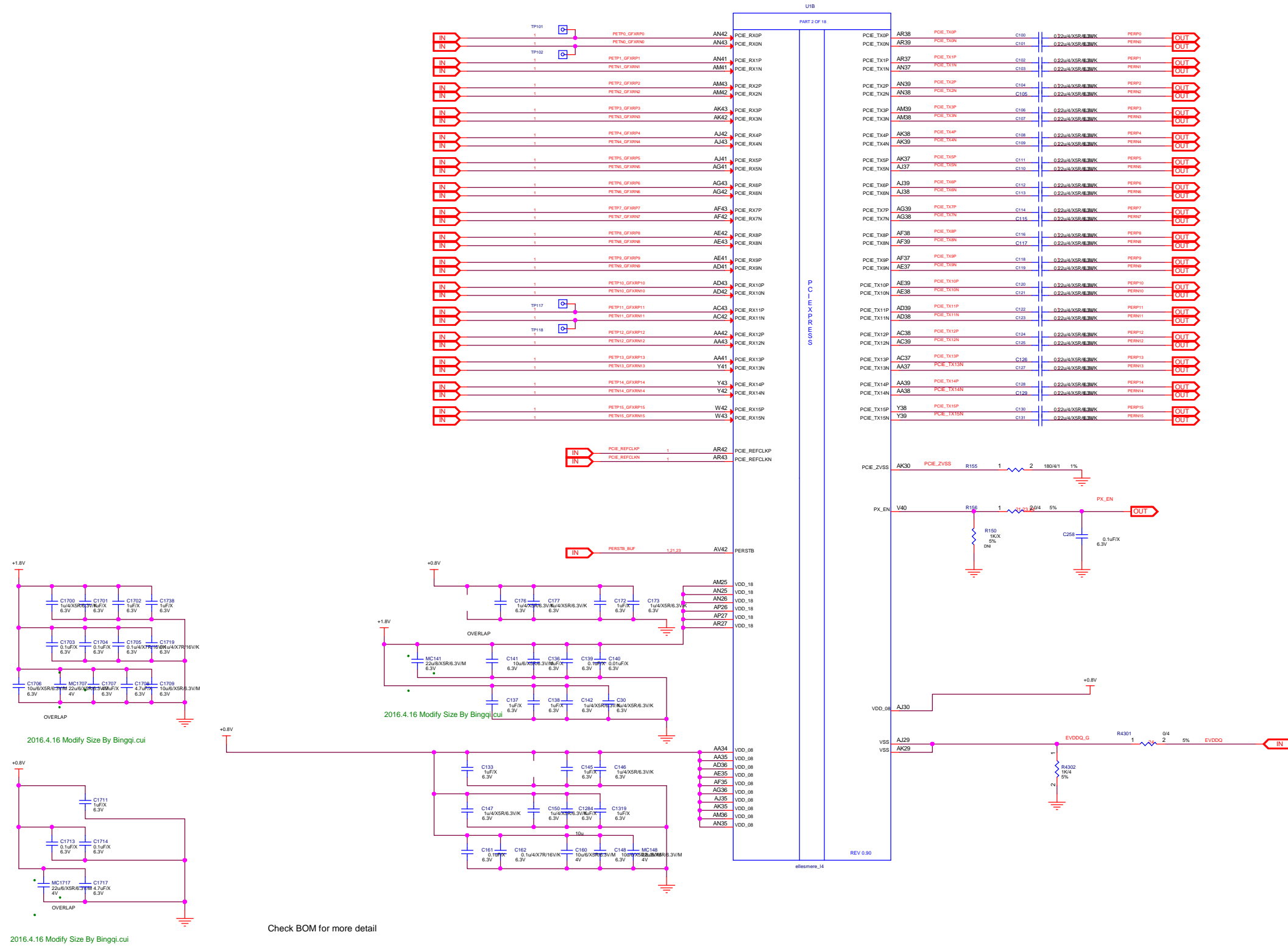


## (1) PCI-EXPRESS EDGE CONNECTOR

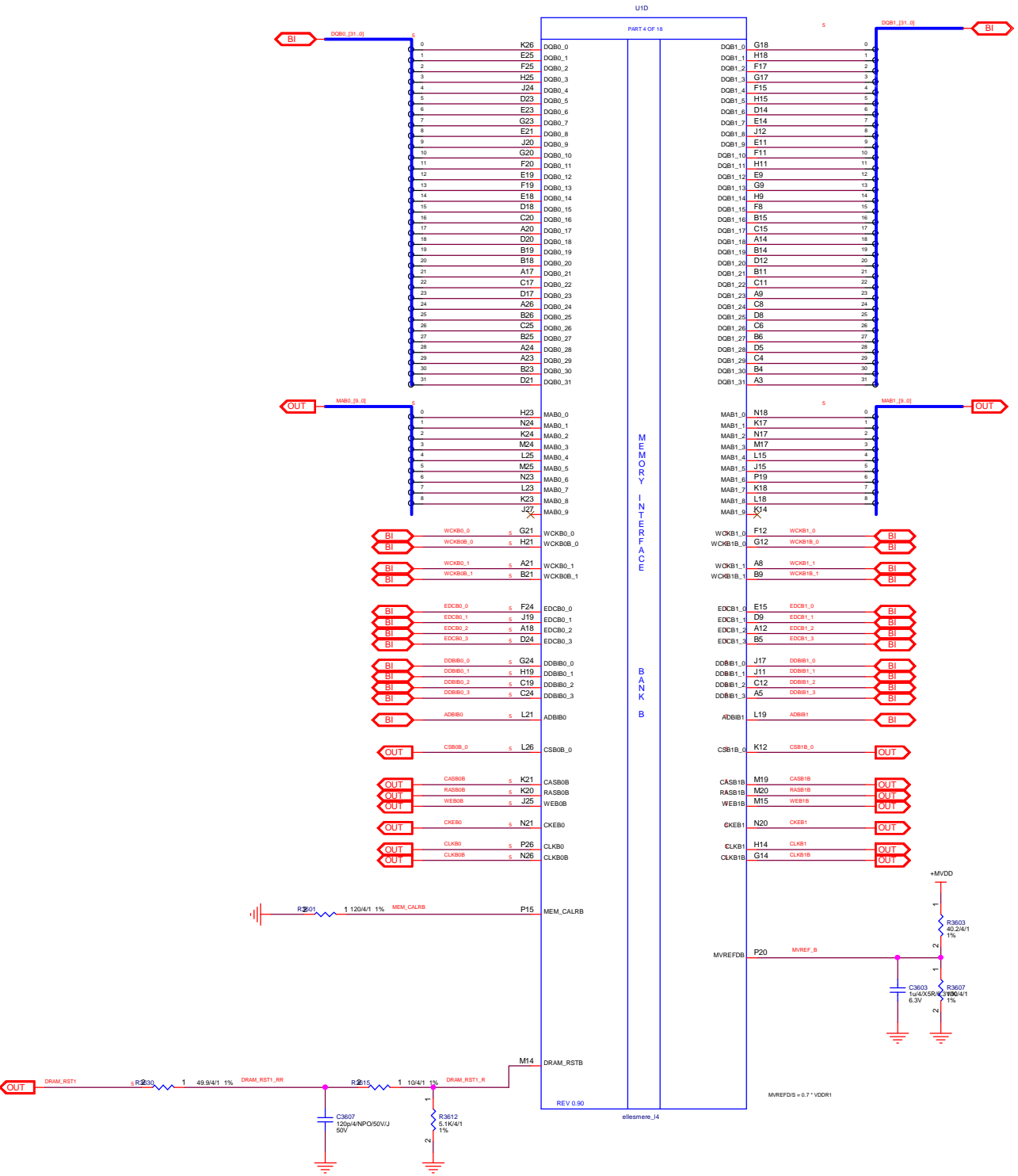
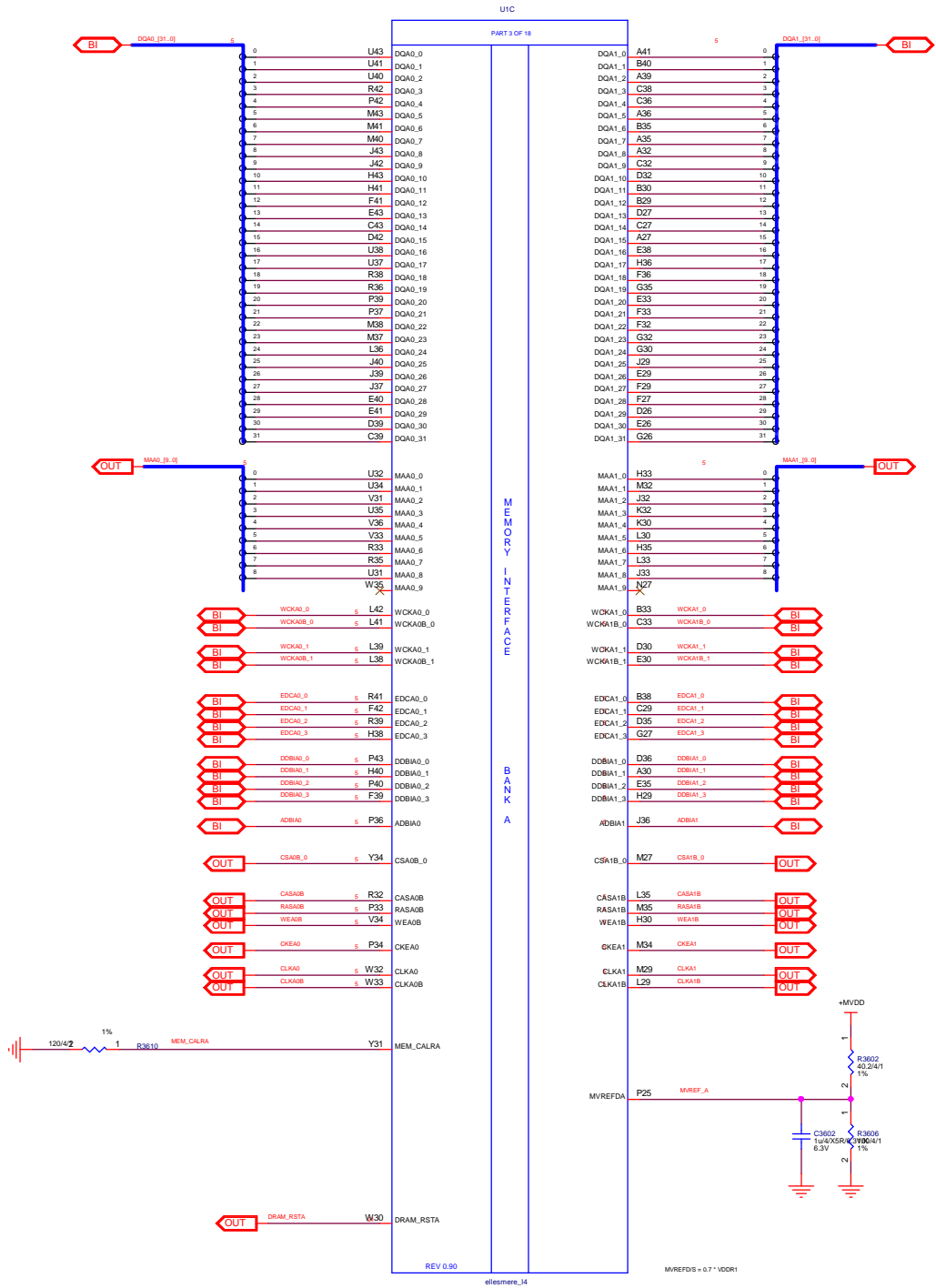


SYMBOL LEGEND	
DNI	DO NOT INSTALL
D or #	ACTIVE LOW
BUO	BRING UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

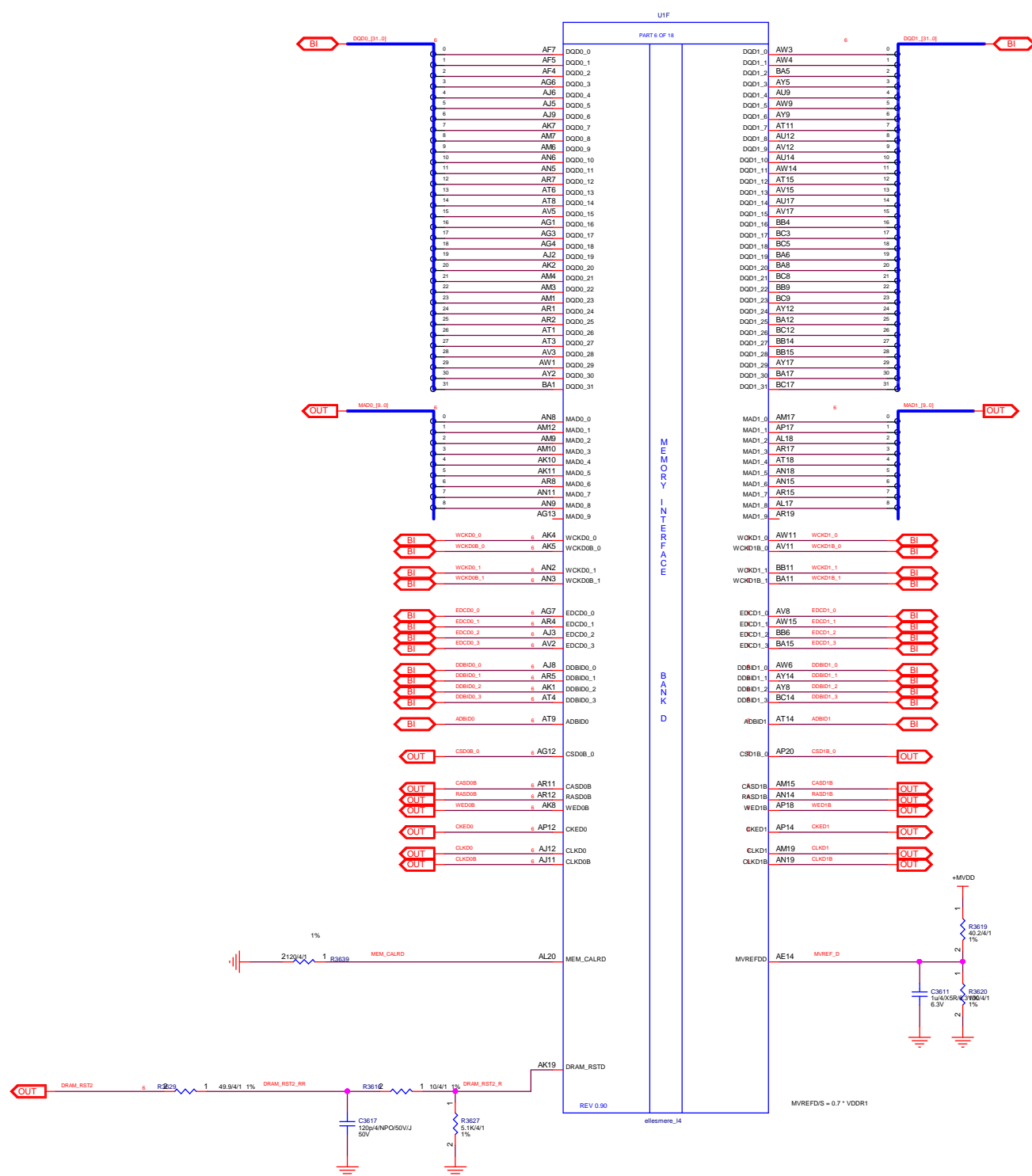
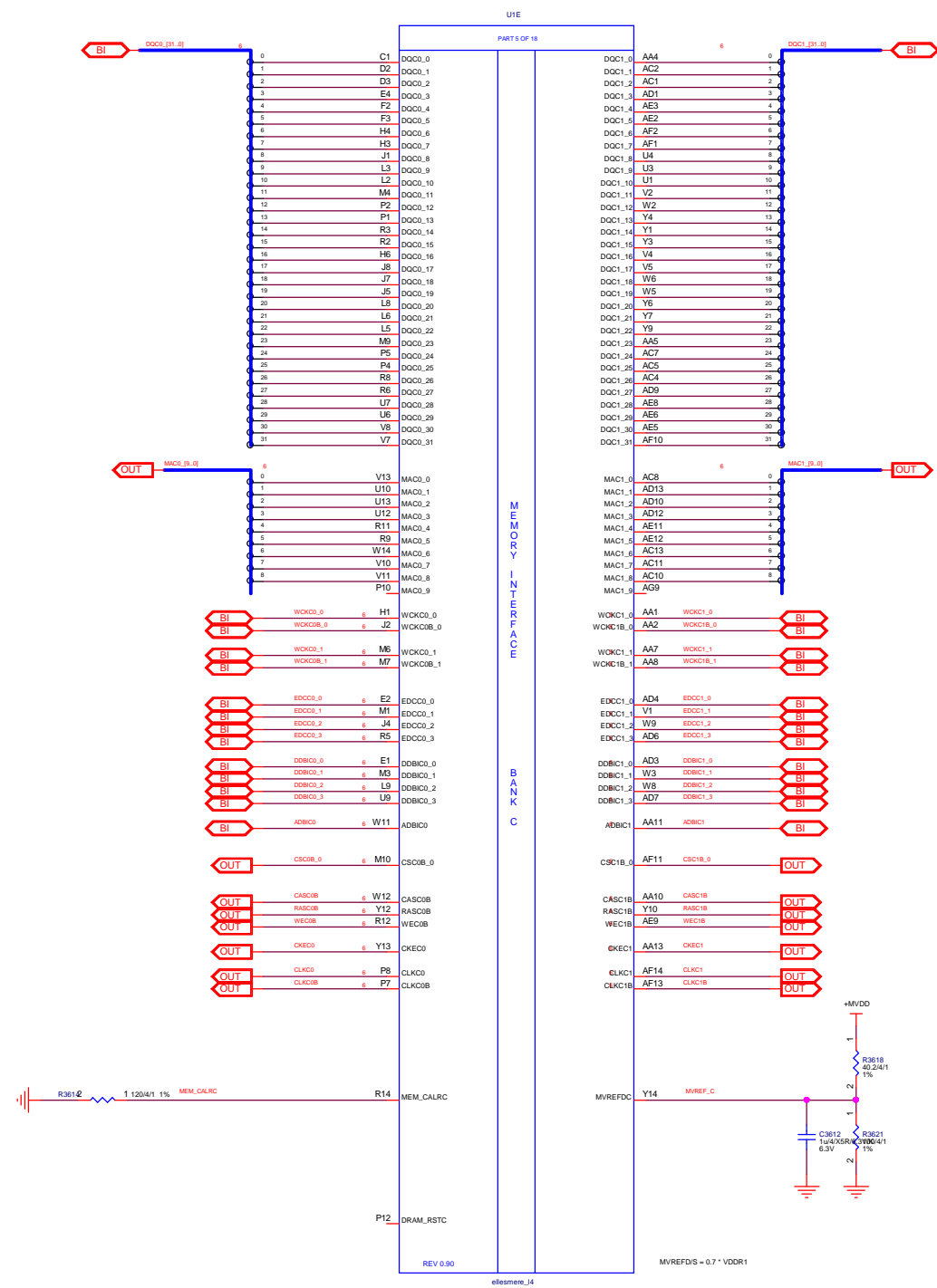
## (2) ELLESMERE PCIE INTERFACE



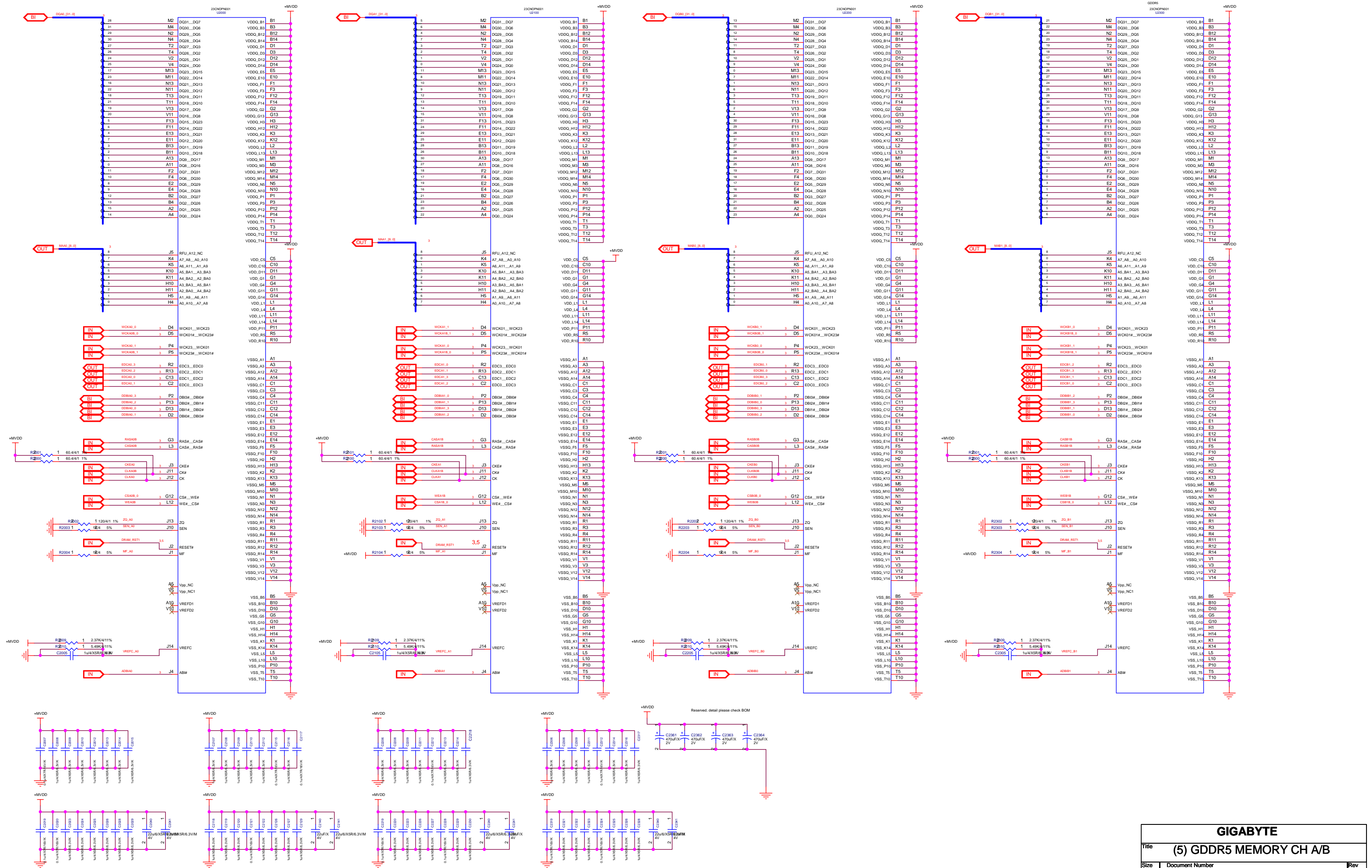
(3) ELLESMERE MEM INTERFACE CH A/B



#### (4) ELLESMERE MEM INTERFACE CH C/D



## (5) GDDR5 MEMORY CH A/B



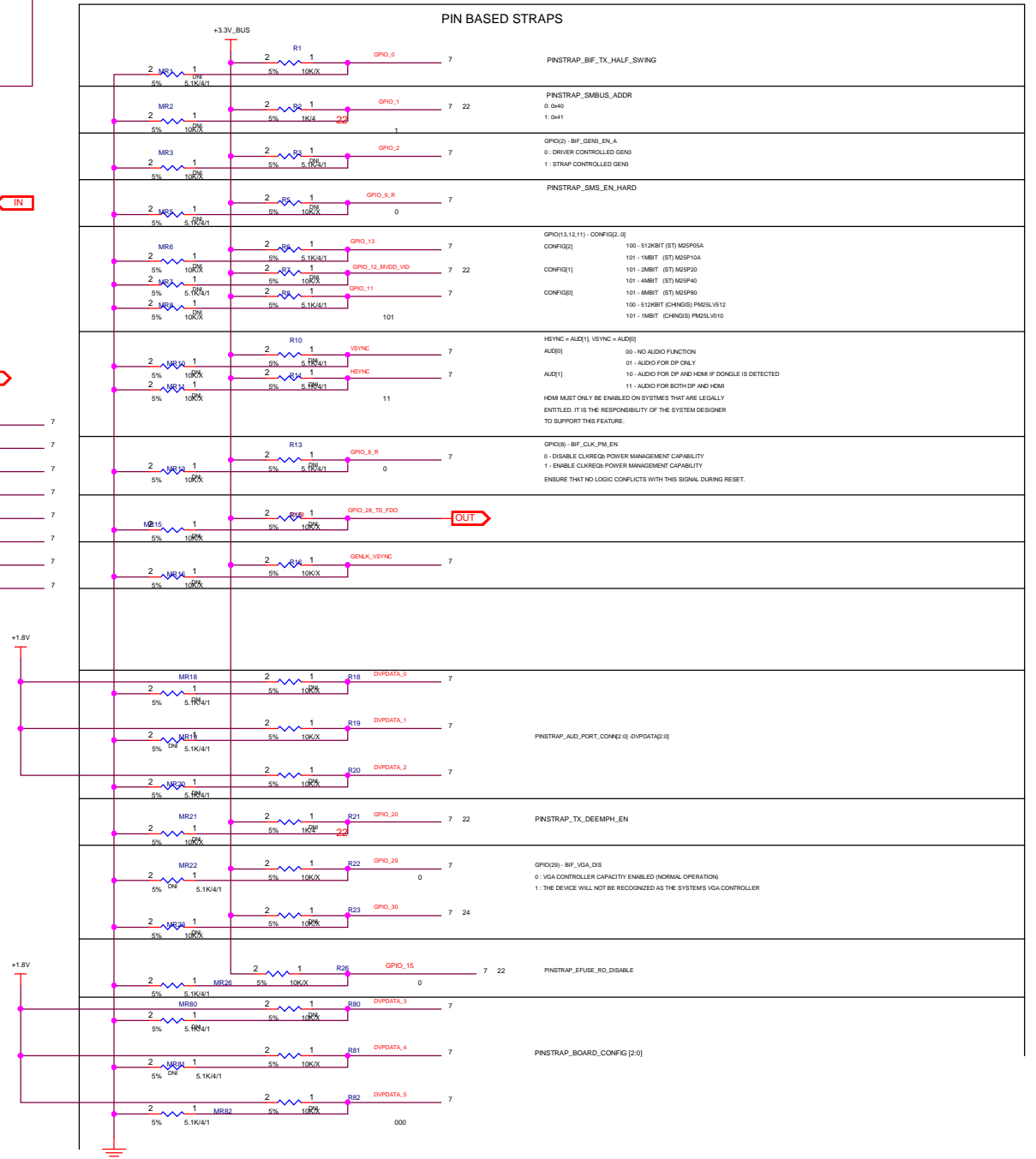
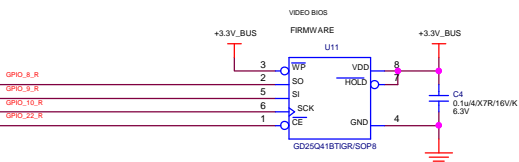
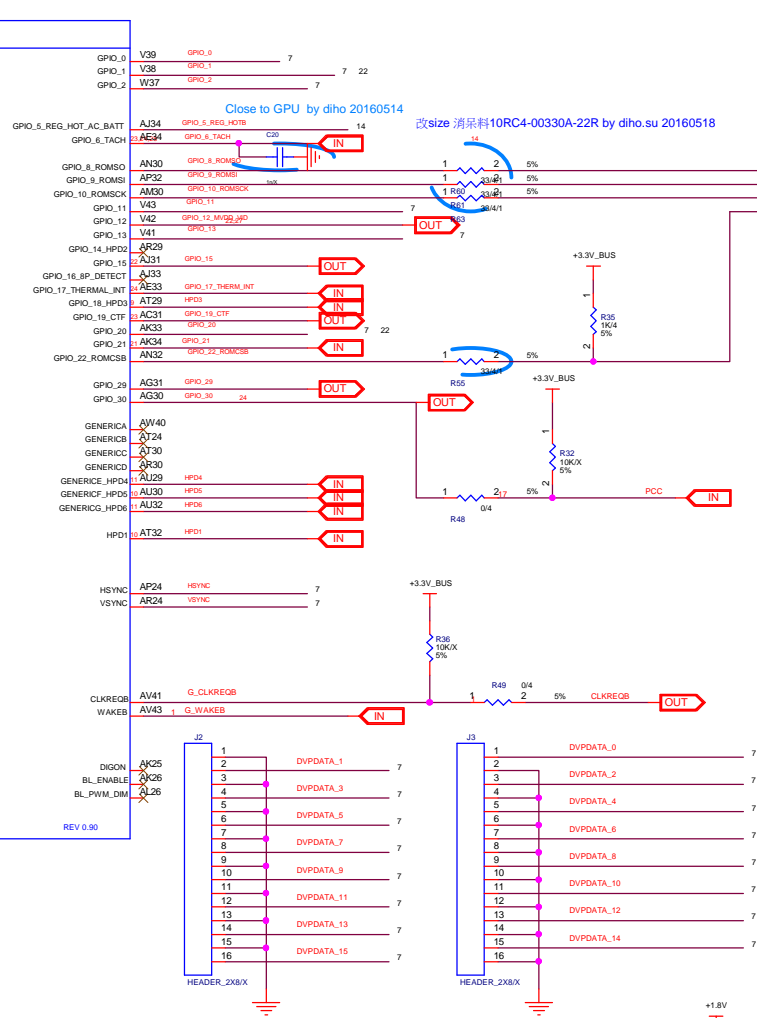
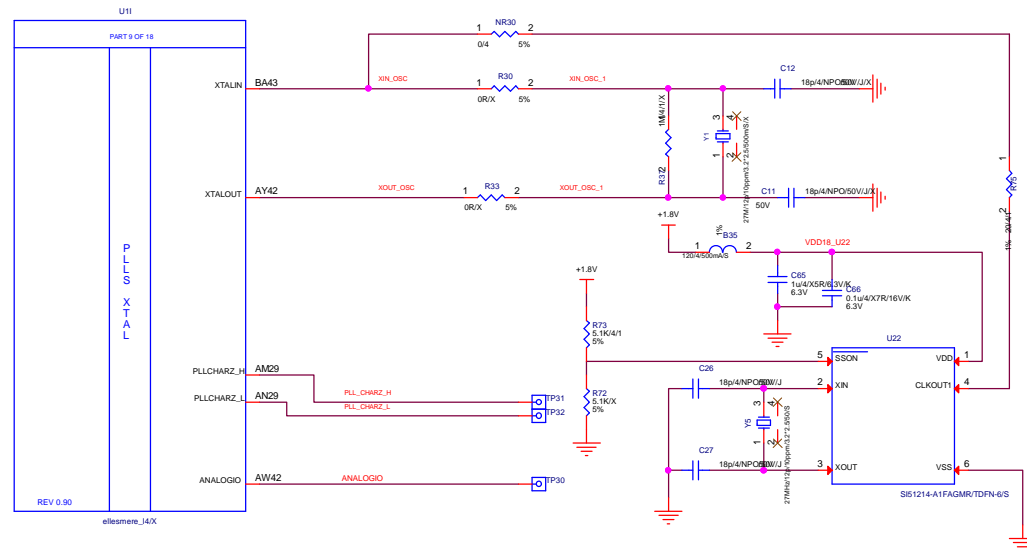
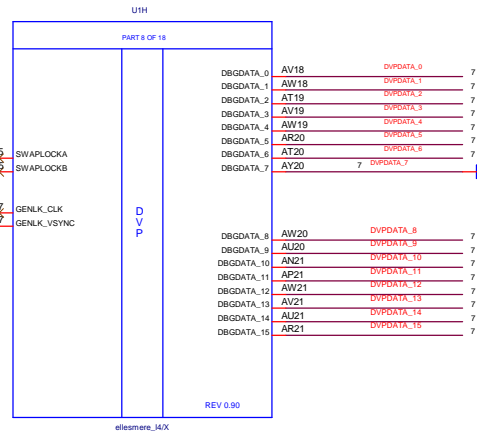
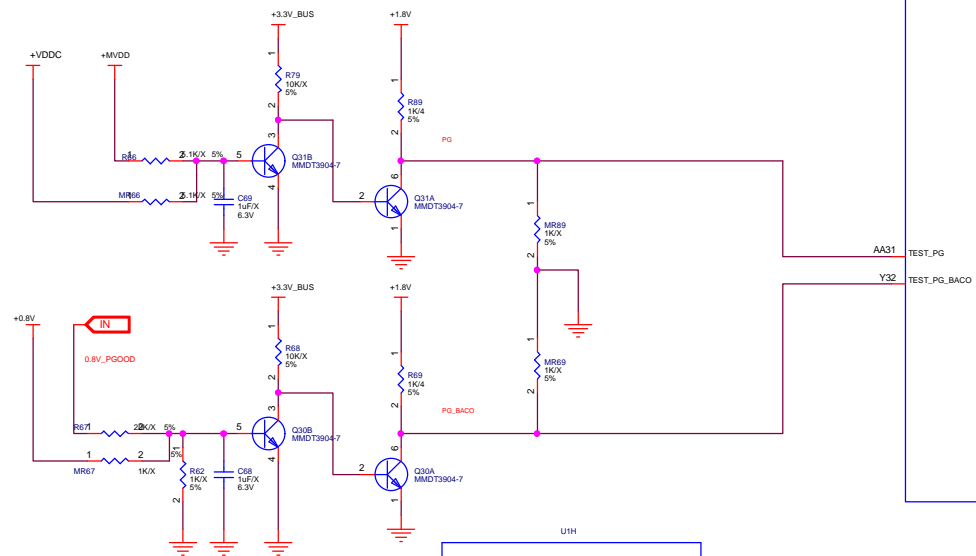
## (6) GDDR5 MEMORY CH C/D



(7) ELLESMERE GPIO STRAP CF XTAL

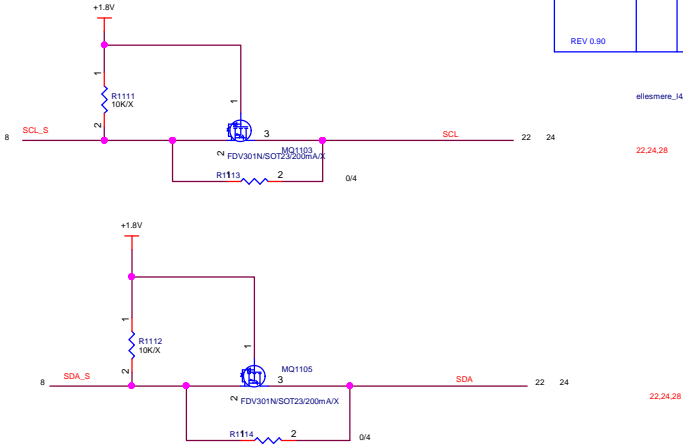
SCI/SDA BUS:		
I2C ADDRESS	FUNCTION	DEVICE

I2C ADDRESS	FUNCTION	DEVICE
0x98	EXT TEMP SENSOR	LM96063





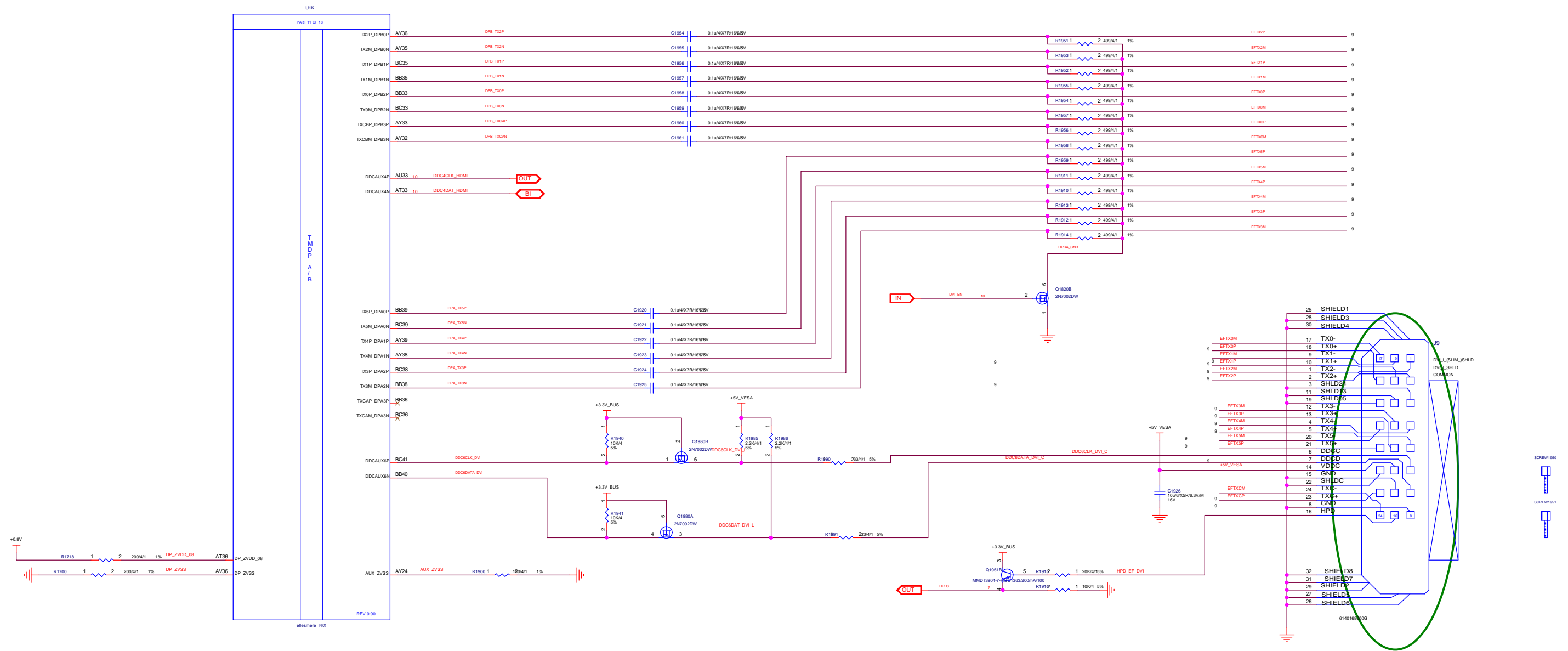
(8) ELLESMERE DAC1 LOCK



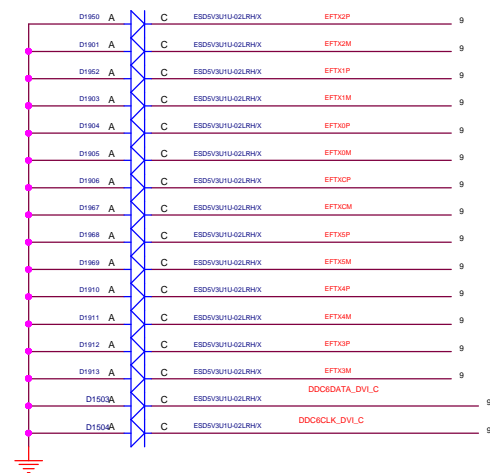
<b>GIGABYTE</b>			
Title ELLESMEERE DAC1 LOCK			
Size	Document Number		Rev
Custom	GV-RX570GAMING-8GD-MI		1.11
Date:	Edwlv	lrrq/L1/2018	US-hel 8 of 20

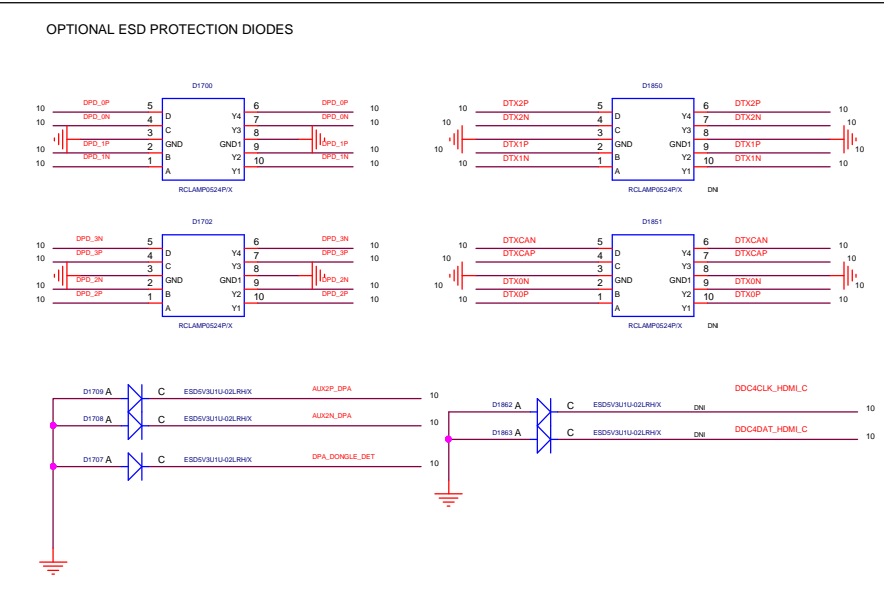
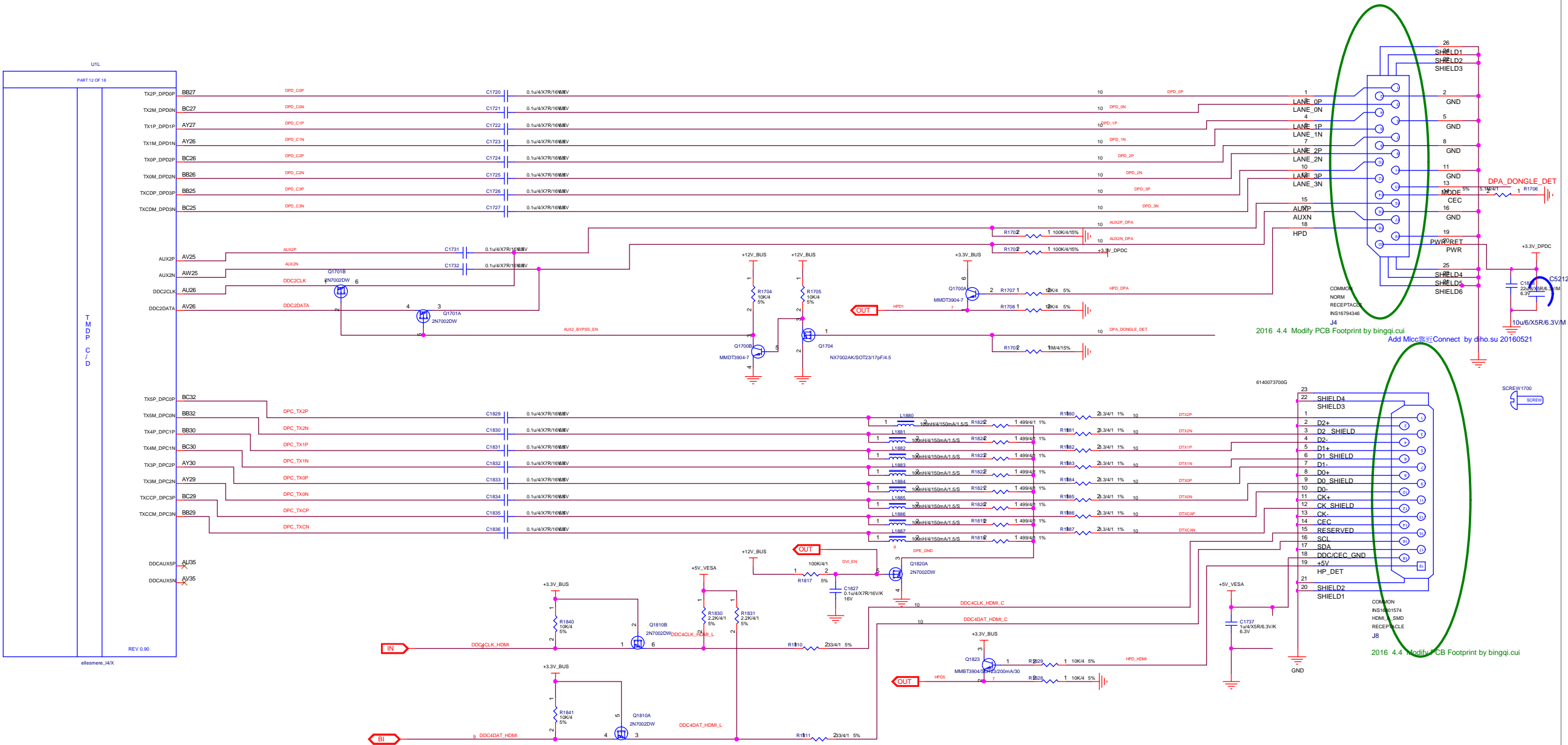


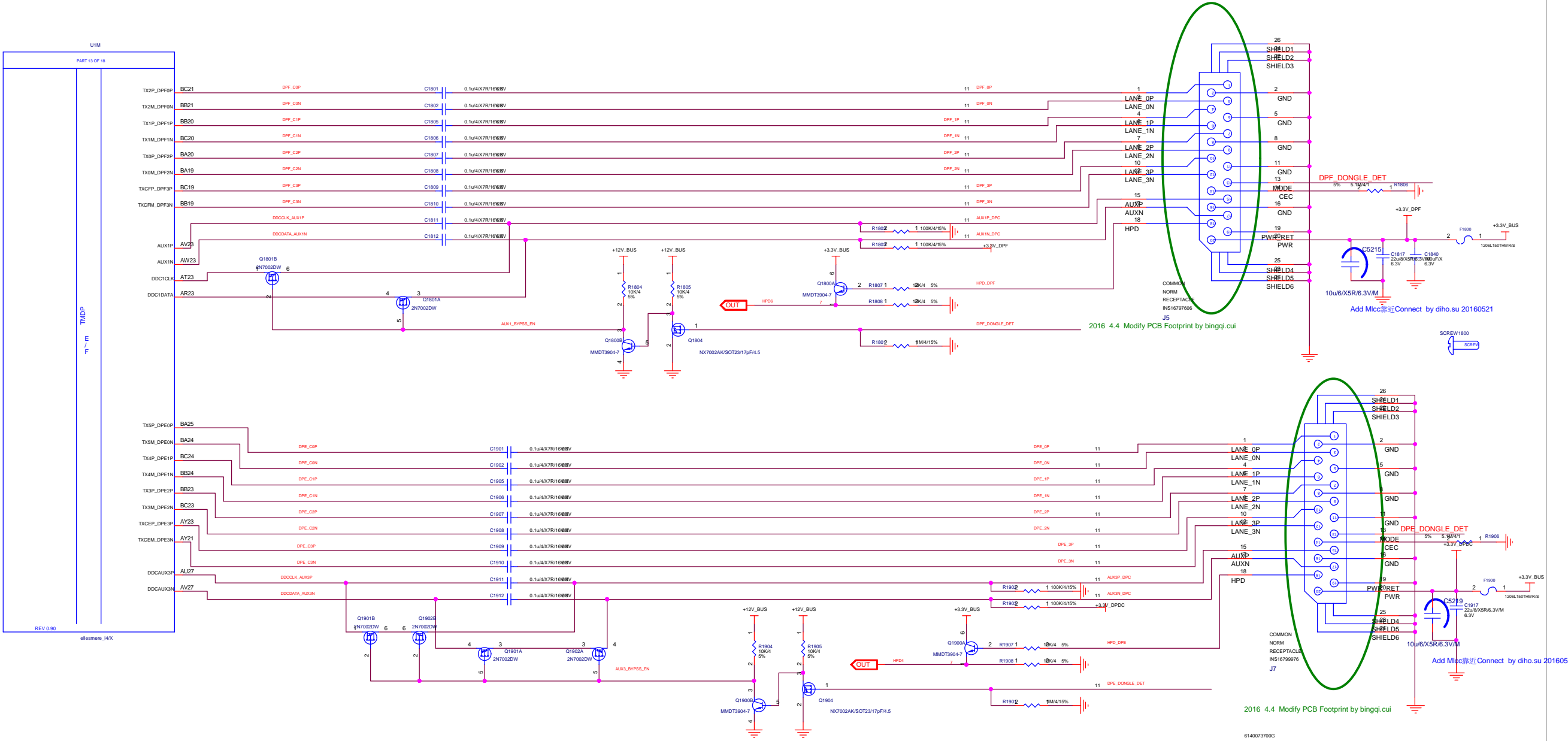
(9) ELLESMERE TMDP A/B



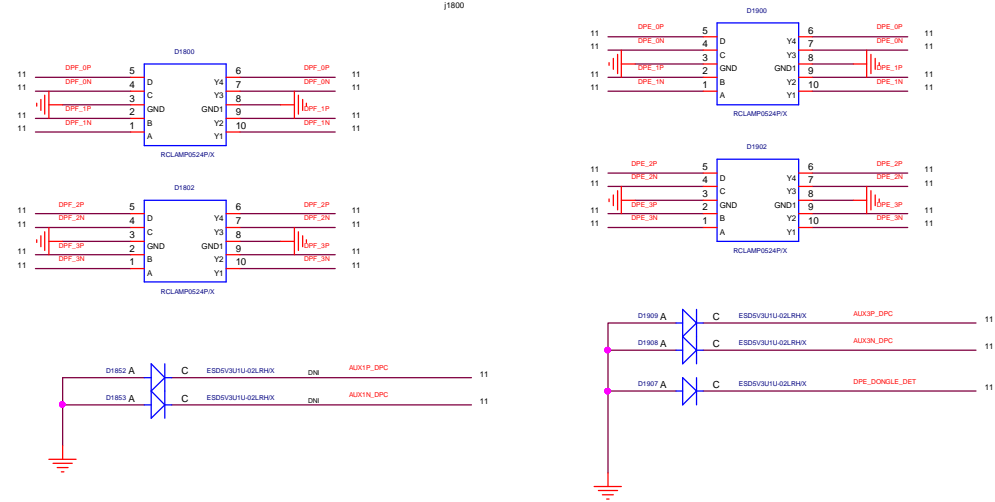
### OPTIONAL ESD PROTECTION DIODES





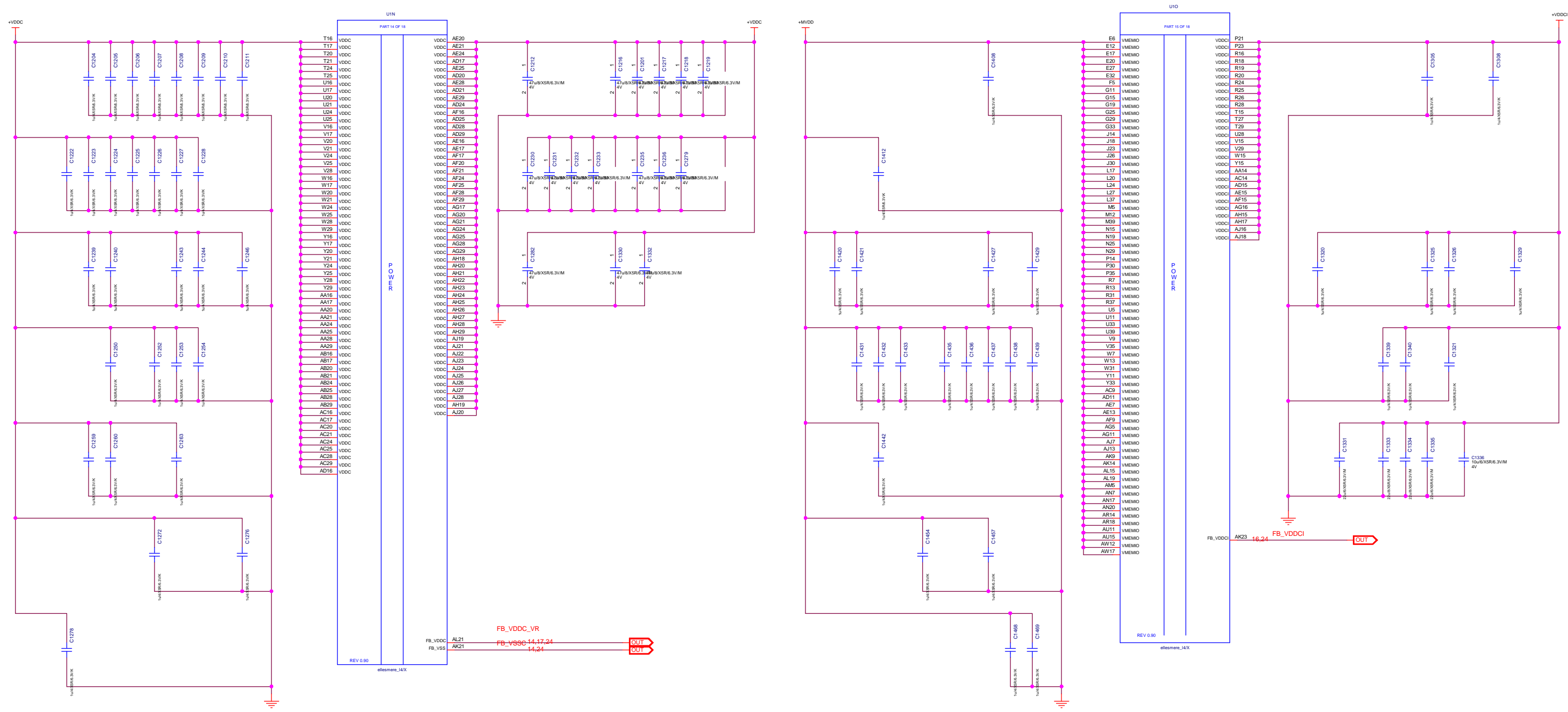


OPTIONAL ESD PROTECTION DIODES

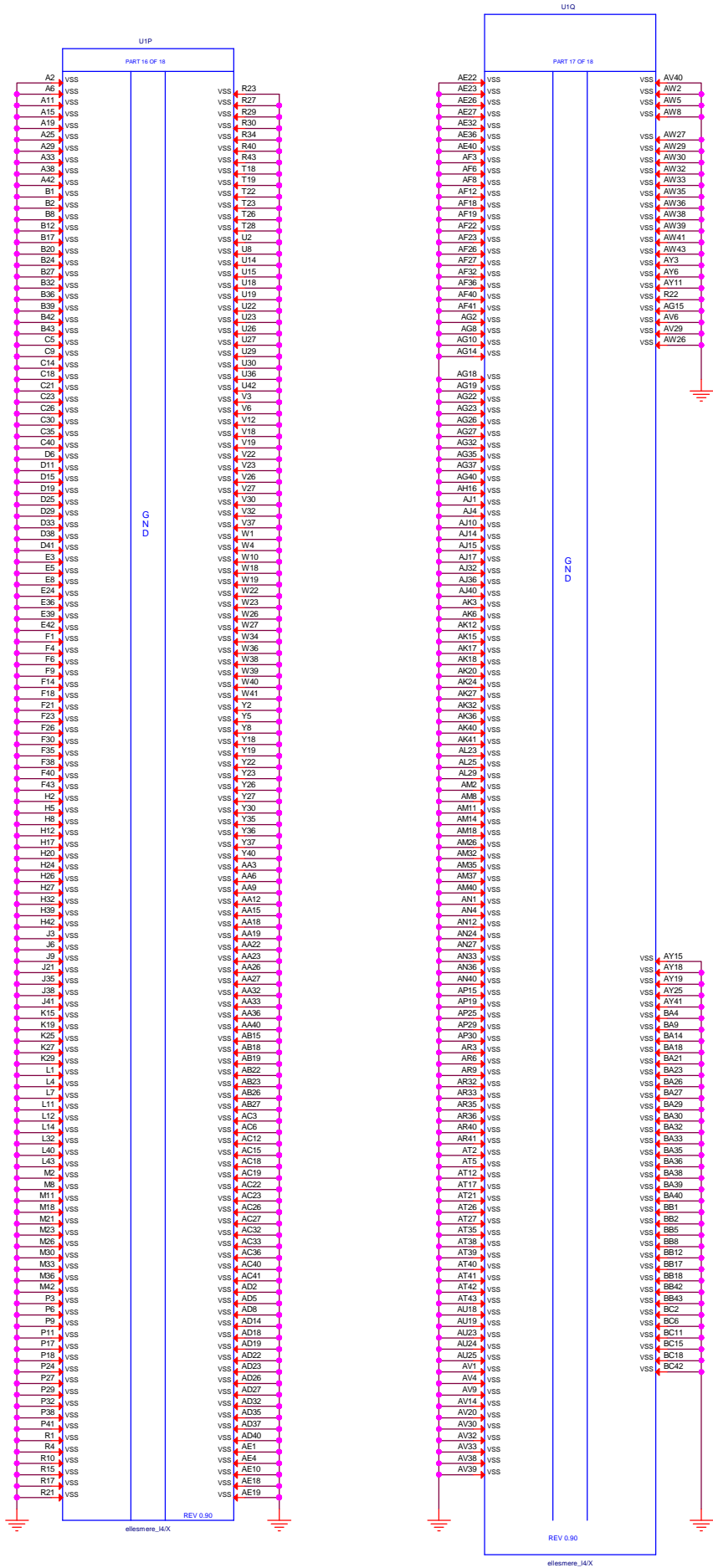


GIGABYTE			
Title ELLESMERE LVTMDP E/F			
Size Custom	Document Number GV-RX570GAMING-8GD-MI		Rev 1.11
Date: Friday, June 01, 2018	Sheet 11	of 29	

(12) ELLESMERE POWER

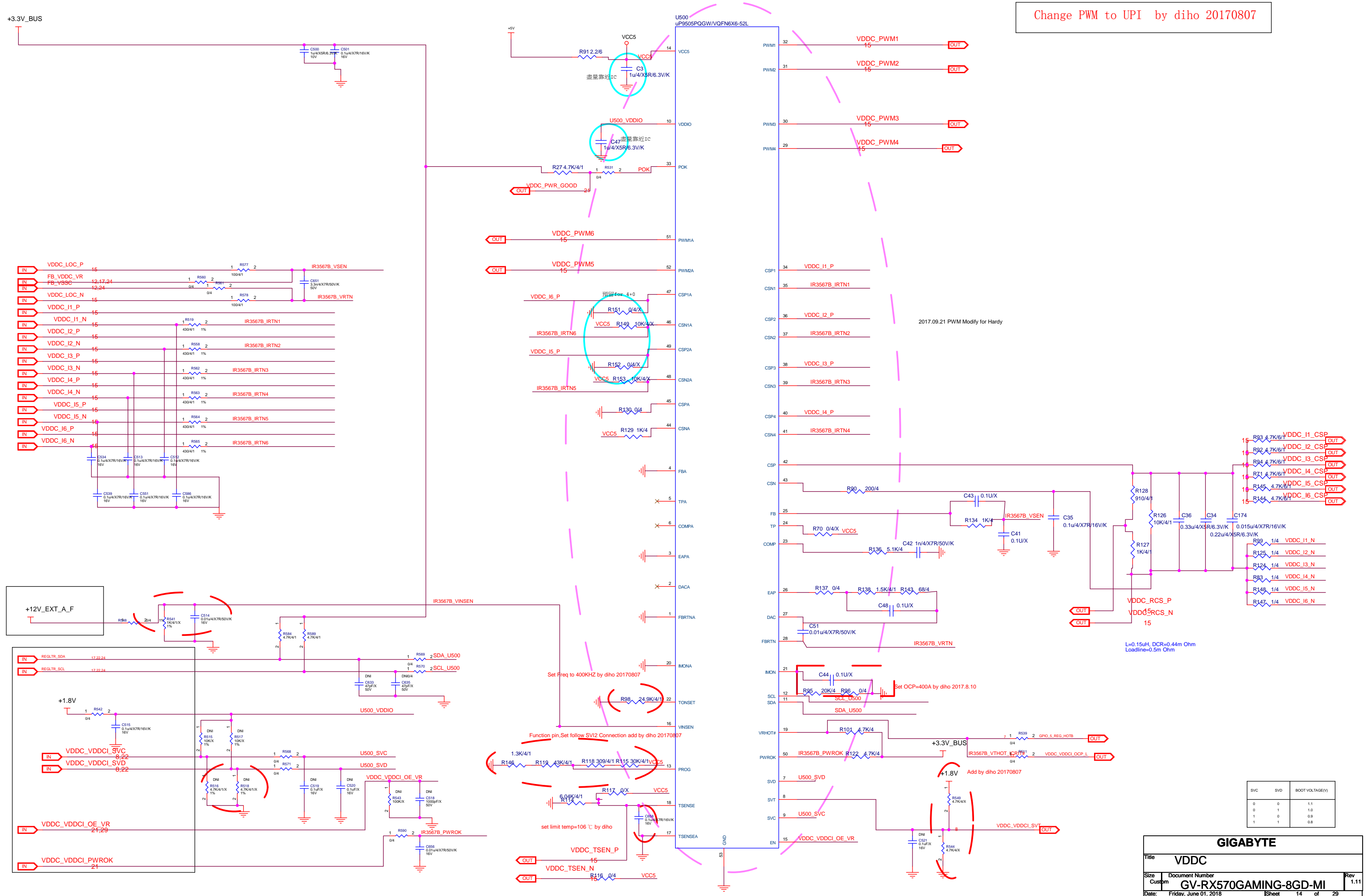


(13) ELLESMERE GROUND



GIGABYTE			
Title ELLESMERE GND			
Size Custom	Document Number GV-RX570GAMING-8GD-MI		Rev 1.11
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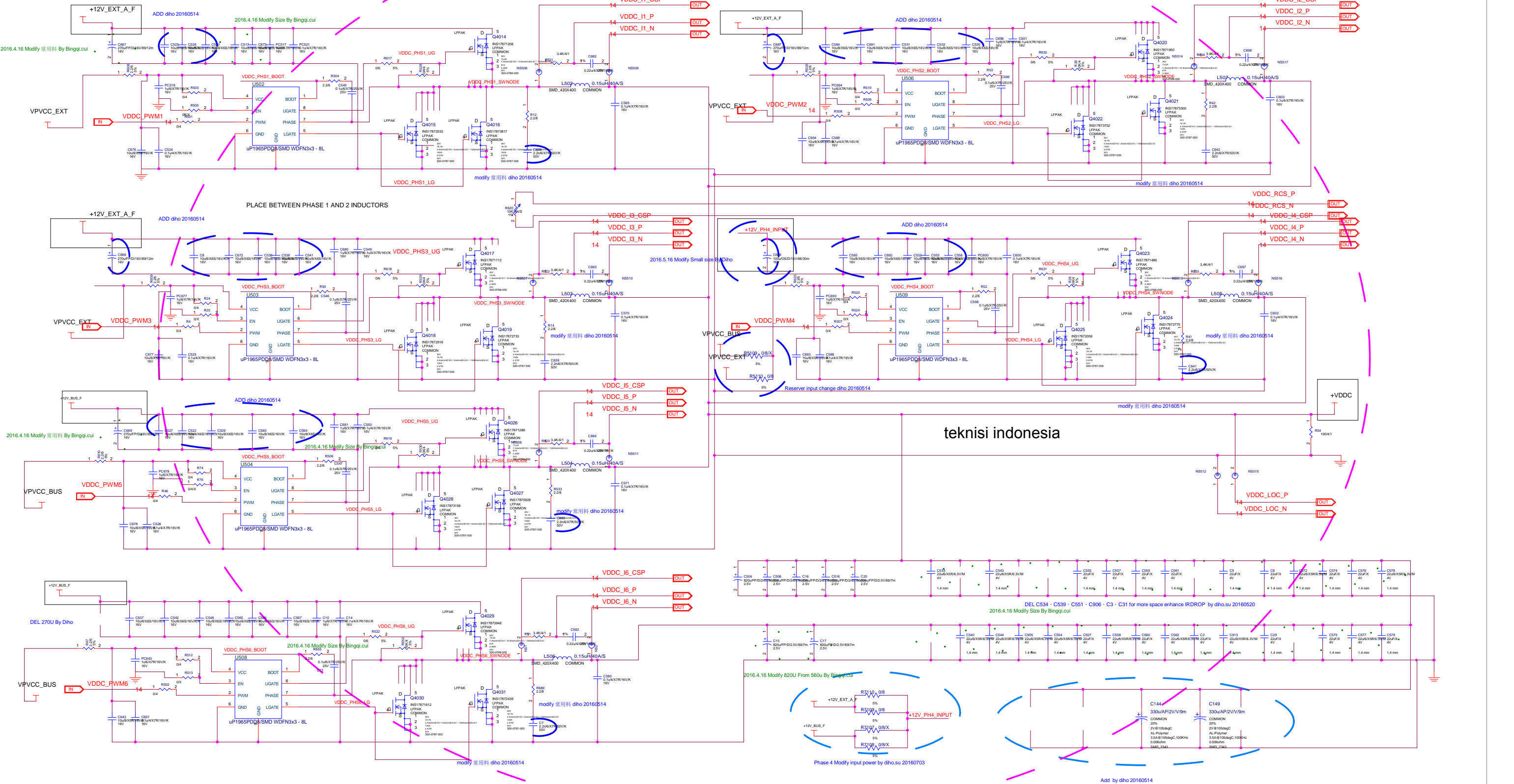
Change PWM to UPI by diho 20170807



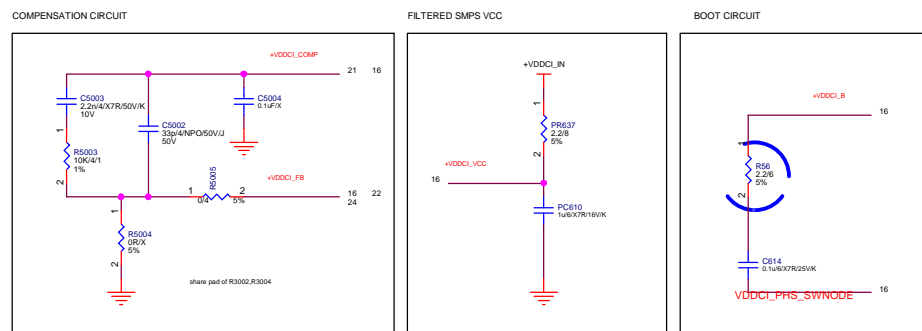
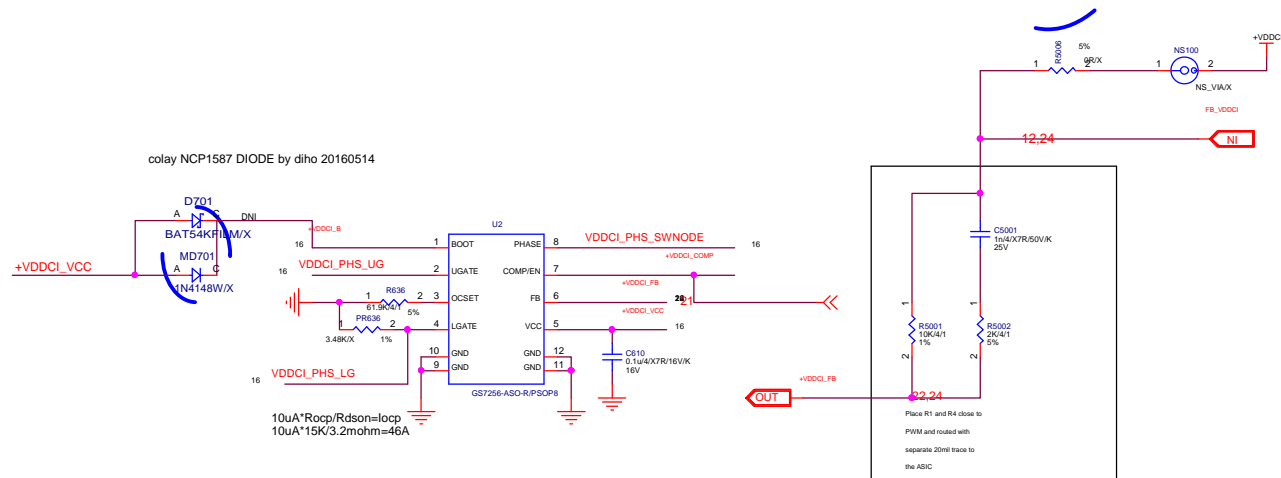
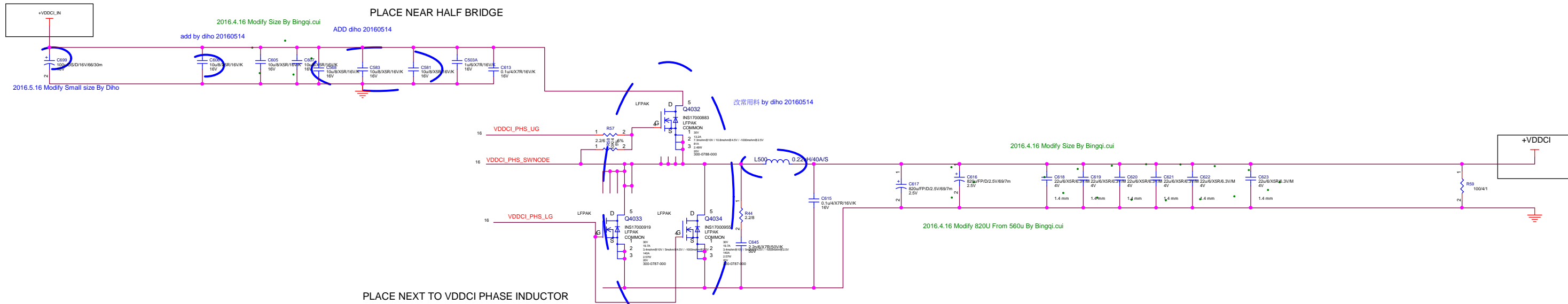
SVC	SVD	BOOT VOLTAGE(V)
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8

2017.09.21 Driver form RX580aorus-4GD Modify for Hardy

PLACE NEXT TO PHASE 3 HALF BRIDGE

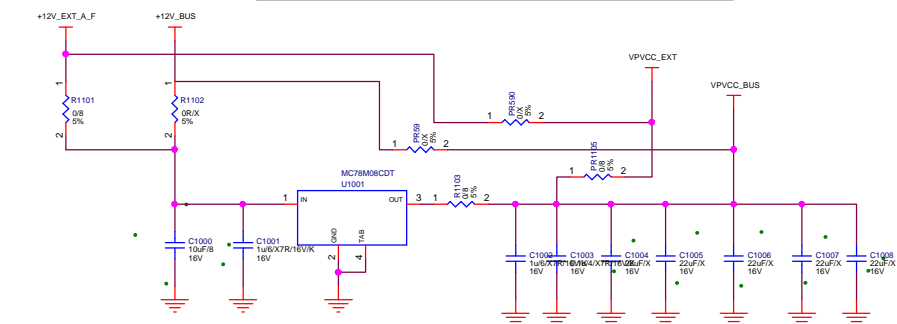






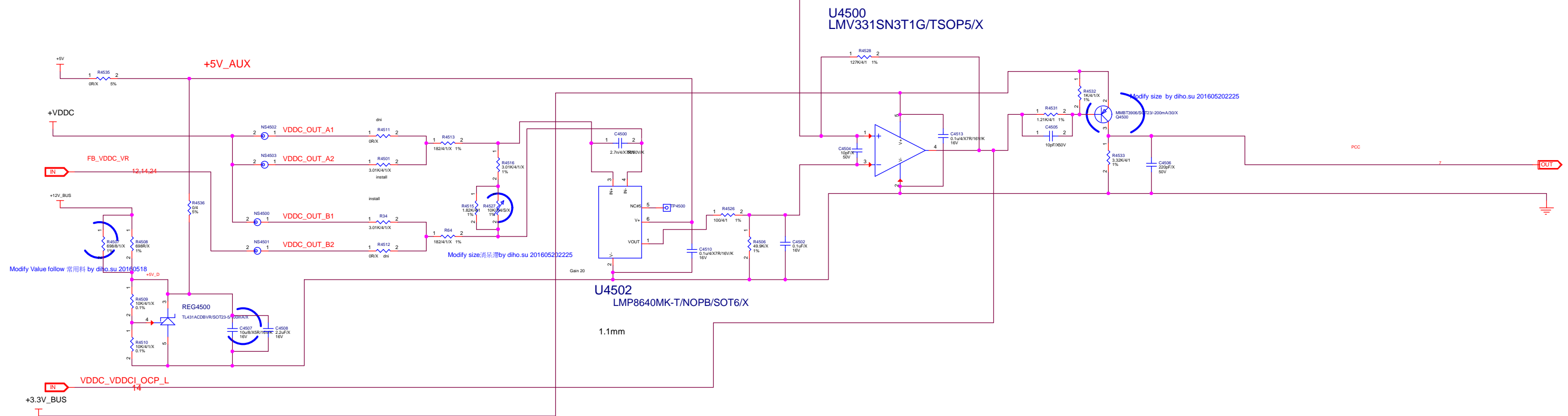
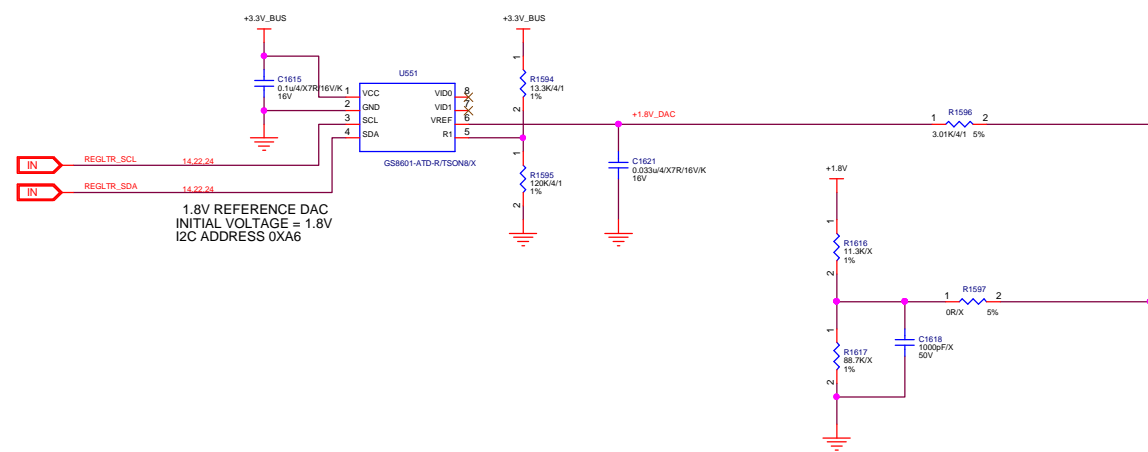
#### REGULATOR FOR VPVCC RAILS

IOUT MAX = 500mA

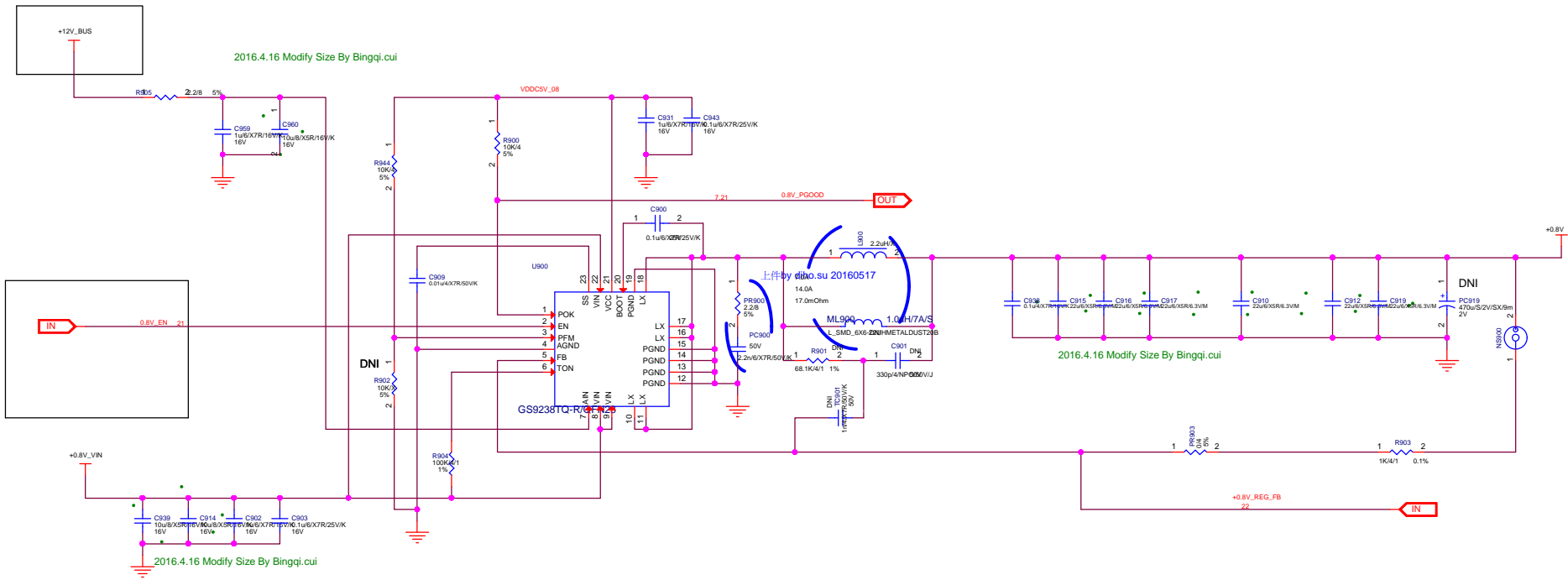


GIGABYTE

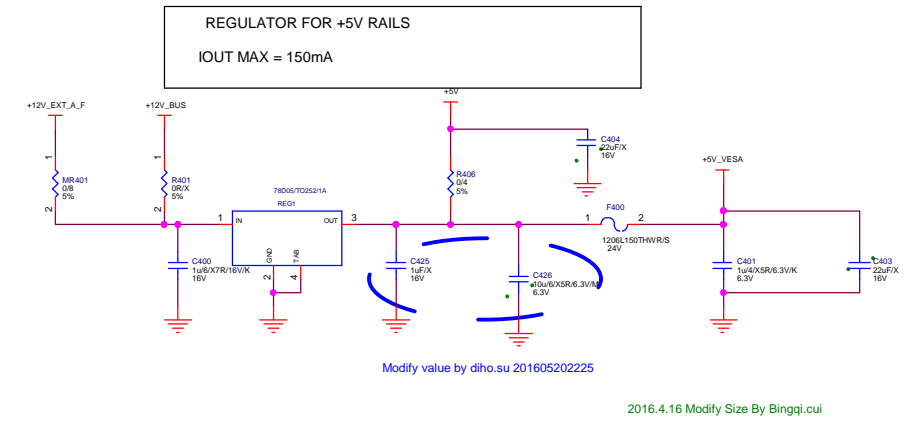
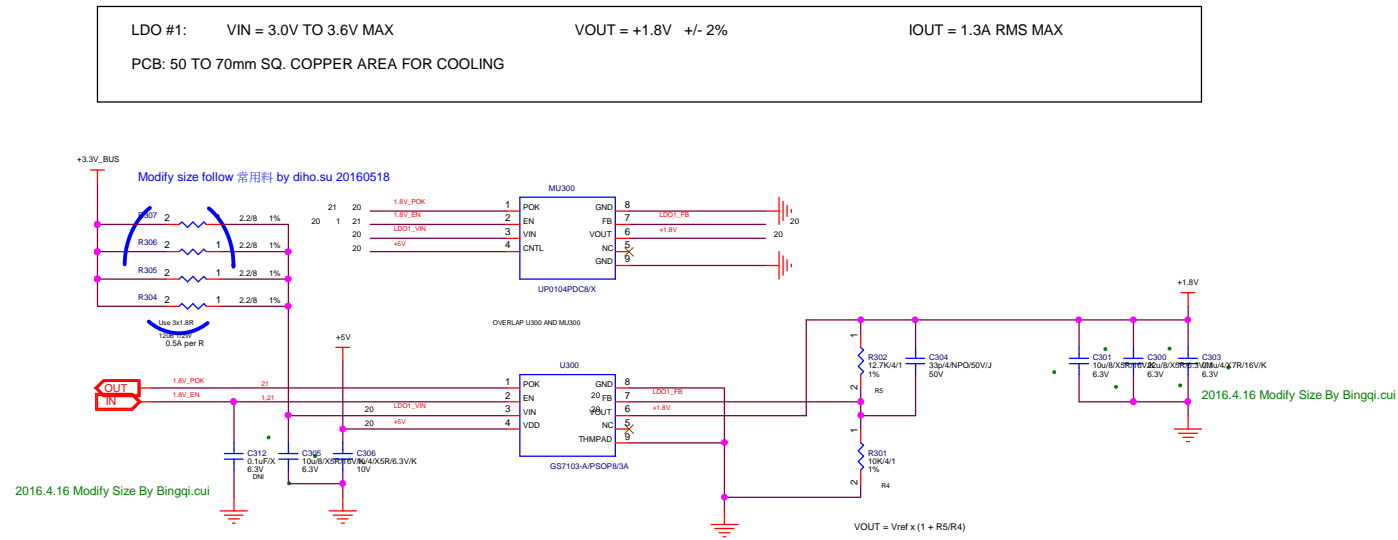
Title	VDDCI
Size	Custom
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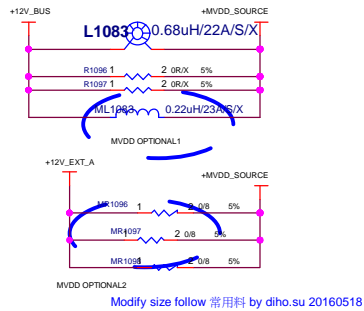
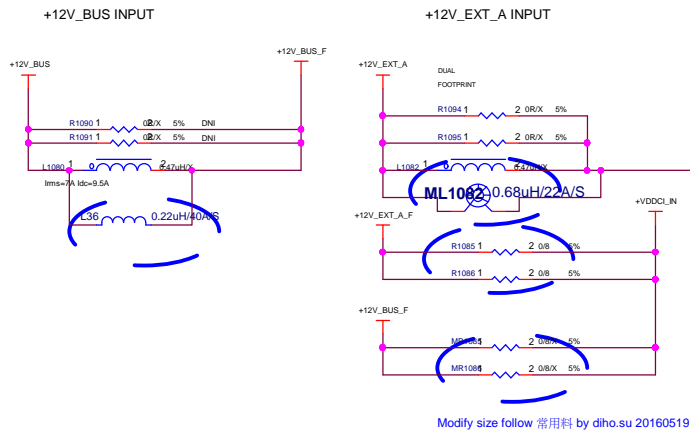
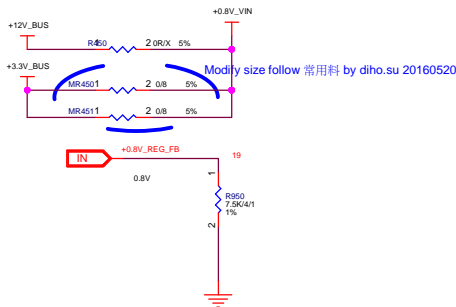
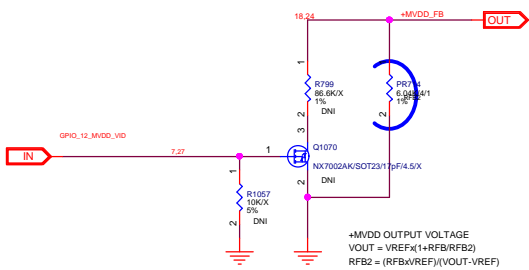
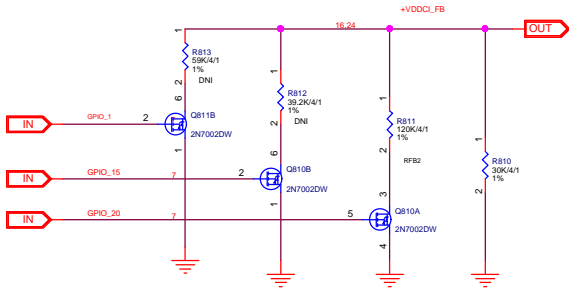
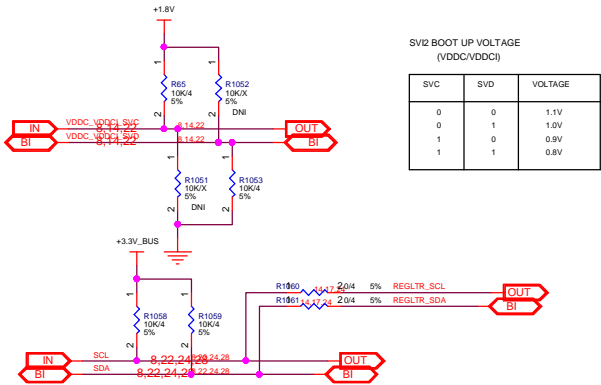




## (18) SMALL RAIL REGULATORS

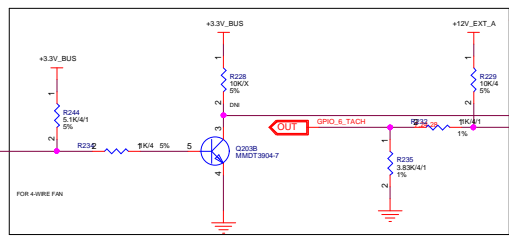




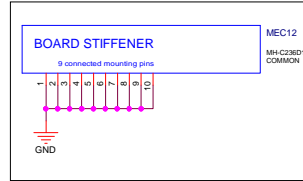




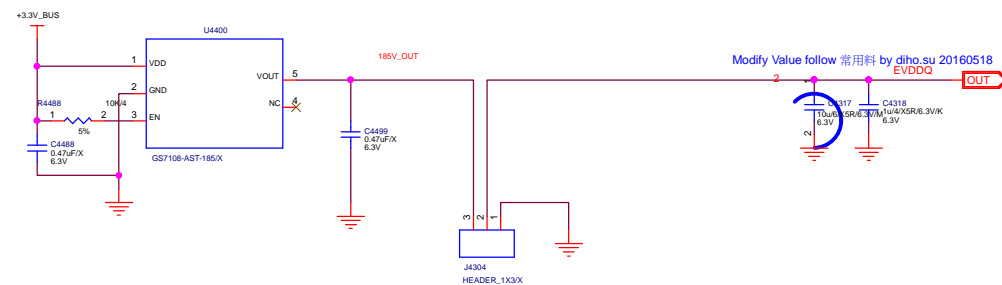
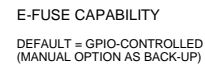
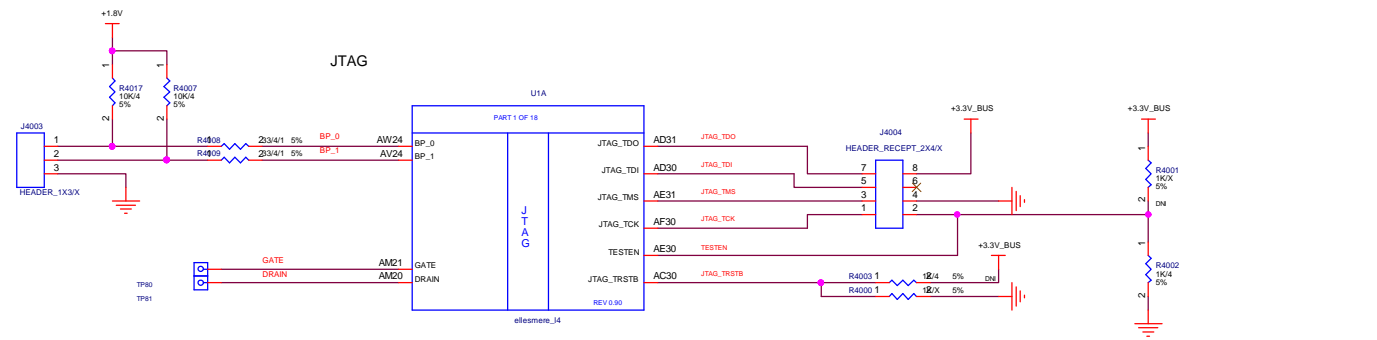
## (20) MECHANICAL AND THERMAL MANAGEMENT



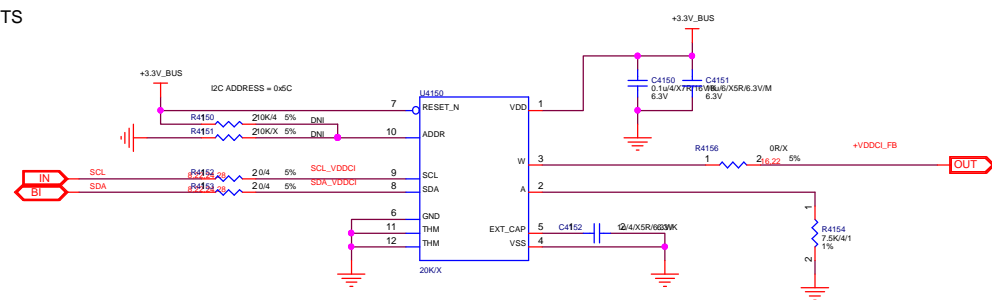
Modify 螺絲孔footprint by diho 20160514



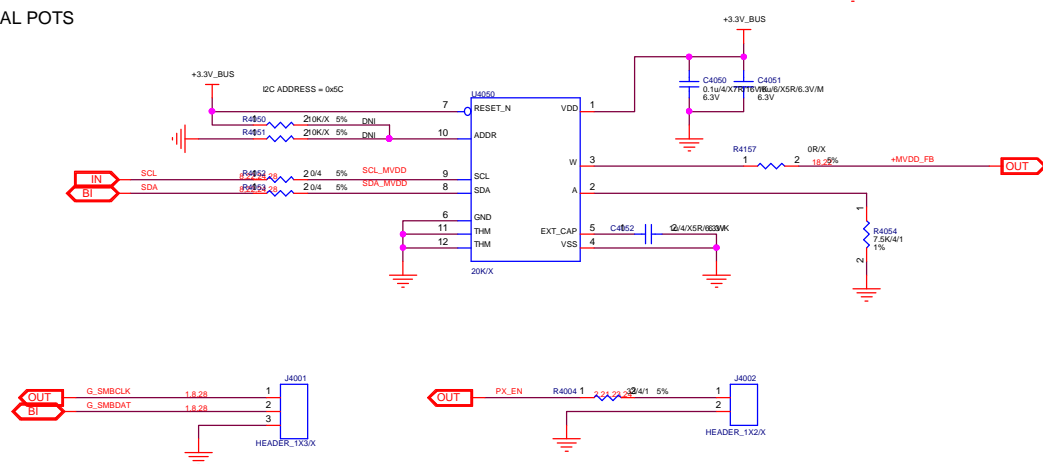
## (21) DEBUG CIRCUITS



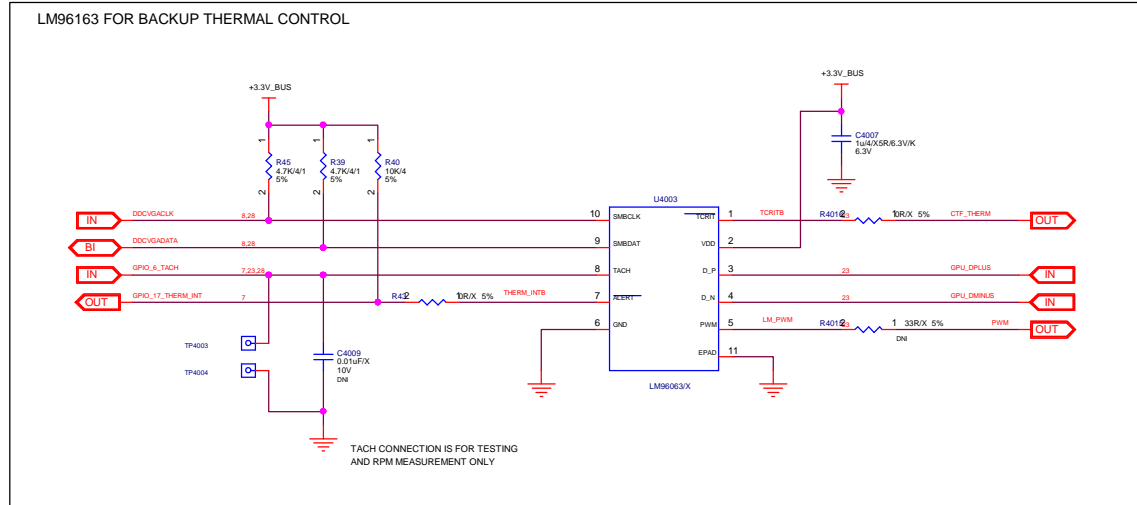
## DIGITAL POTS



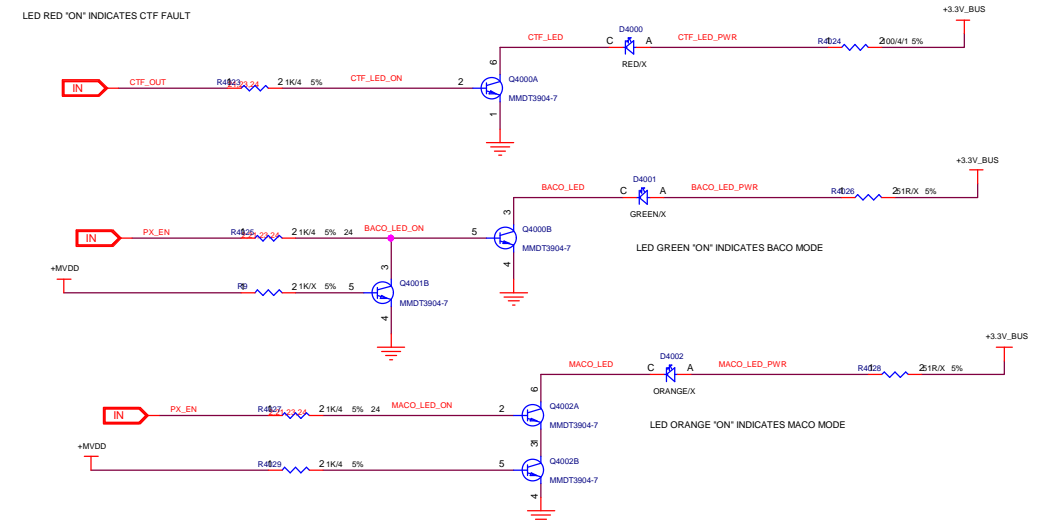
## DIGITAL POTS



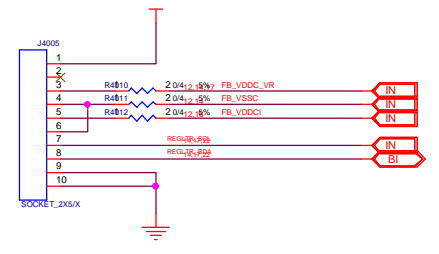
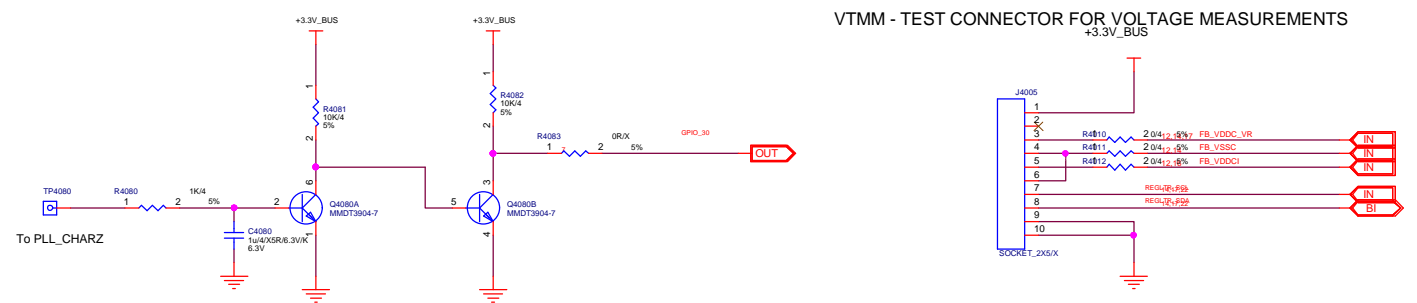
## SWITCHES



## LED LIGHTS



### VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS

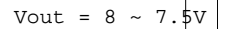


<b>GIGABYTE</b>			
Title <b>DEBUG CIRCUITS</b>			
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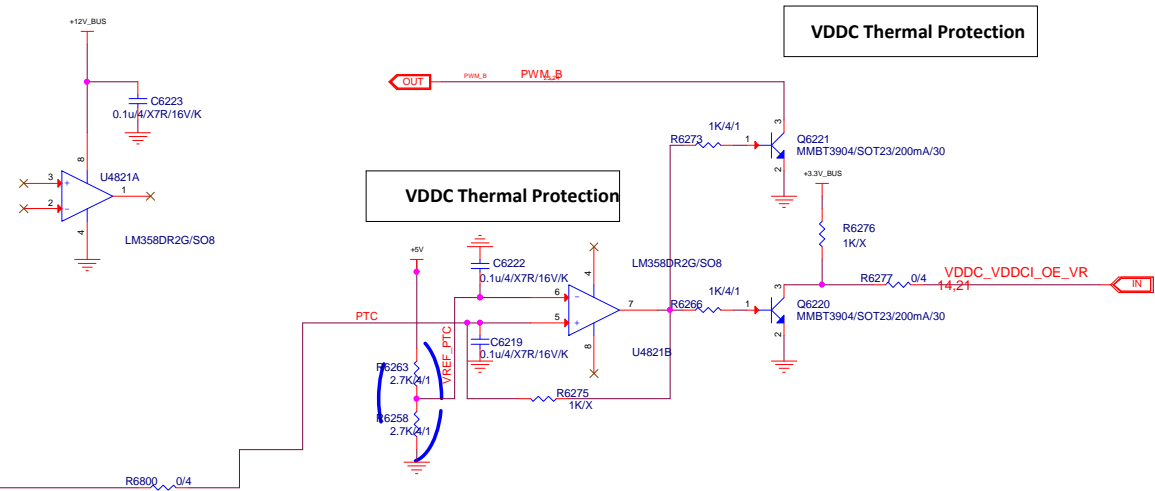
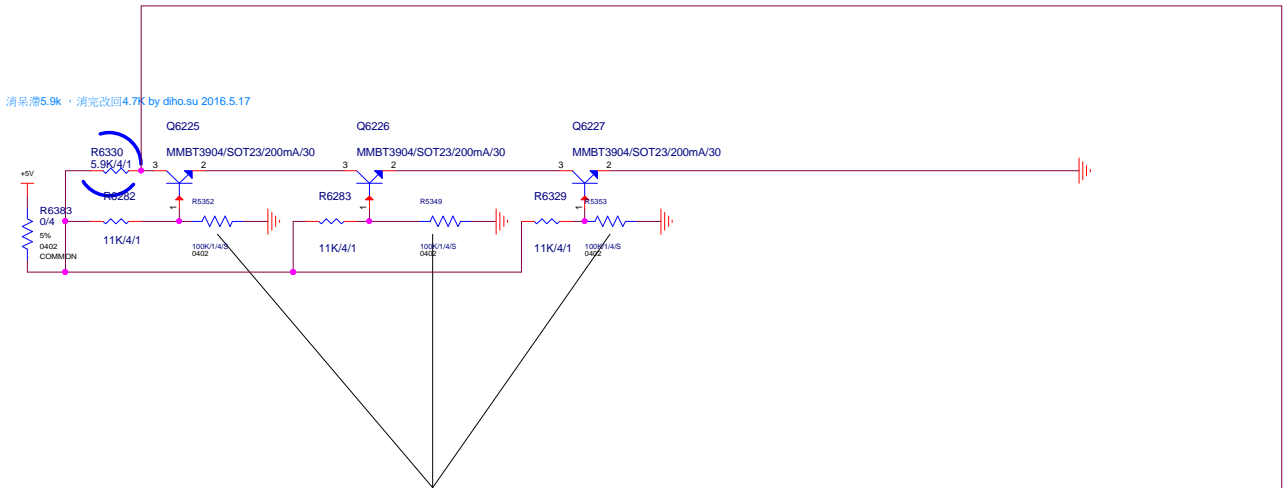
0	00A	08/28/2018	
1	00B	10/18/2018	1. Add GPIOs for dynamic VDDCI 2. update VDDCI driver sequence
2	00C	03/22/2019	HDMI: - add series resistor R1880 ~R1887 - add pull down inductor L1880 ~L1887  remove C405, C403, VR400,C410,C414,R405
3	00D	04/18/2019	Modify GBT Design



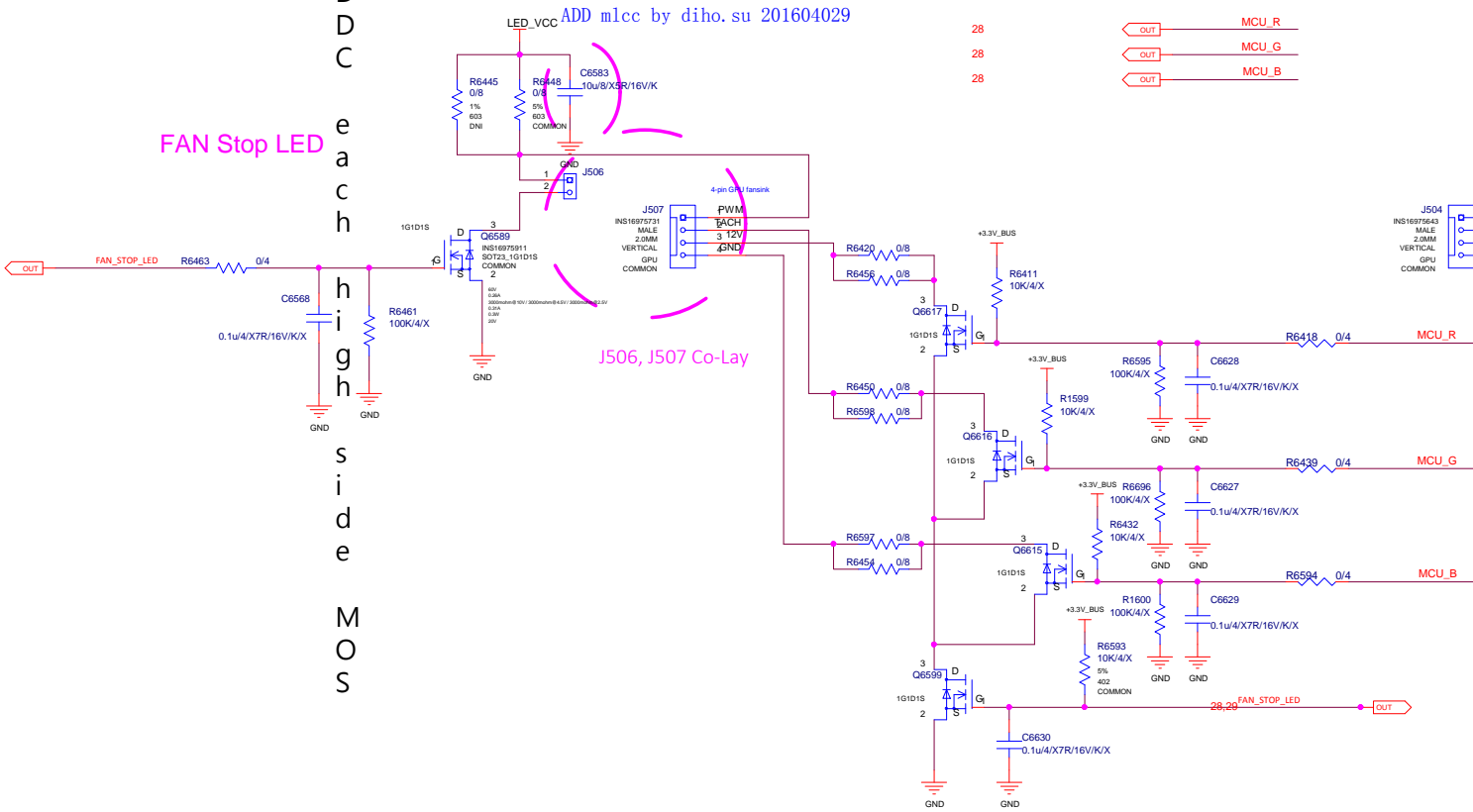


Size		Document Number		Rev	
CustomLED		GV-RX570GAMING-8GD-MI		1.11	
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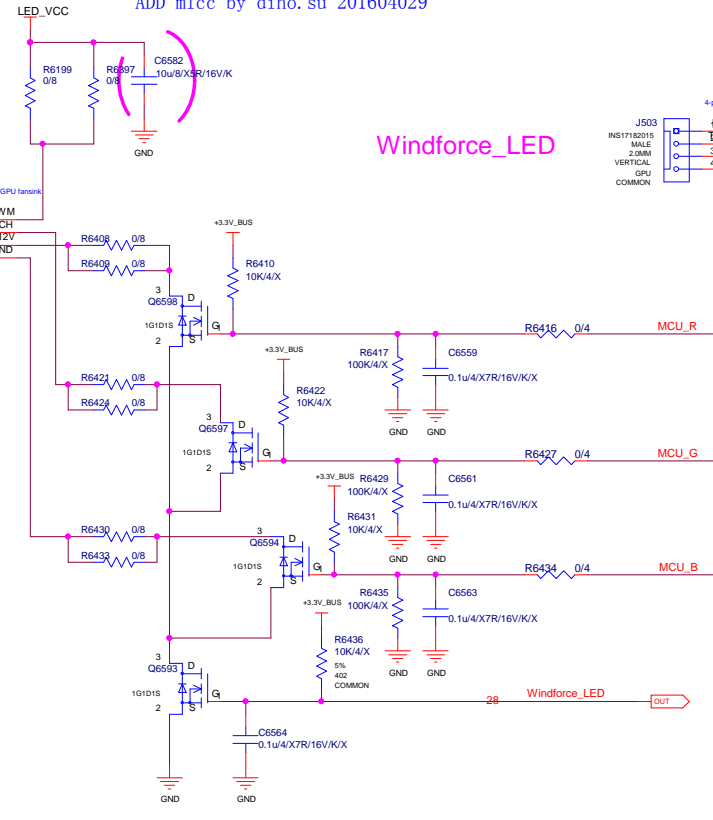
Add MOS VRHOT CIRCUIT



FAN Stop LED

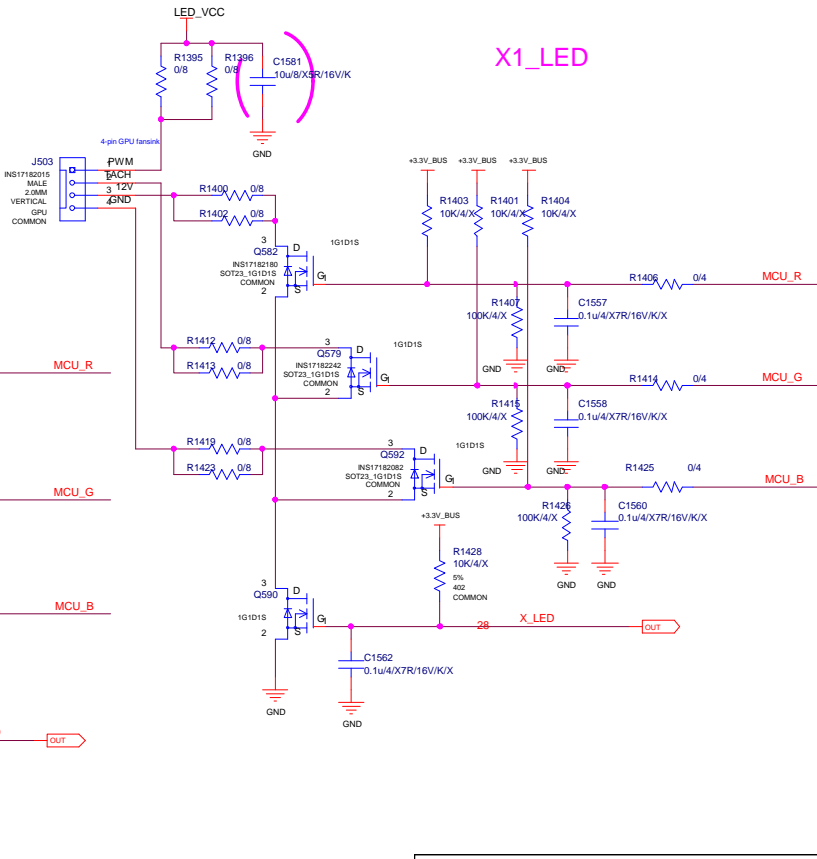


ADD mlcc by diho.su 201604029



Windforce\_LED

X1\_LED



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PAGE DETAIL

MECH: Bracket/Thermal

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